

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)  
15689.67

Application Number  
09/763,375

Applicant(s)  
Yukihiko Okumura

Filing Date  
February 21, 2001

Group Art Unit  
2631

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CO	1	5,896,374	04/20/1999	Okumura et al.	370	311	02/23/1996
CO	2	6,108,384	08/22/2000	Okumura et al.	375	262	06/23/1997

## FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
CO	3	W096/26582	08/29/1996	JAPAN			✓	
CO	4	W097/50219	12/31/1997	JAPAN			✓	

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

CO	5	"CRC position", TSGRI (99) 689, TSG-RAN Working Group I meeting#5, Cheju, Korea, June 1-4, 1999.
CO	6	Ad hoc #4 chair, "Second ad hoc #4 meeting report", TSGRI #5 (99) 723, TSG-RAN Working Group I meeting #5, Cheju, South Korea, June 1-4, 1999

EXAMINER  
Curtis Odum

DATE CONSIDERED  
10/24/05

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INITIAL

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"CR for parity bit attachment to 0 bit transport block", TSGRI #9(99)L34, TSG-RAN Working Group 1 meeting #9, Dresden, Germany, November 30-December 3, 1999

"A scheme to measure quality for outer loop TPC during DPDCH OFF", TSGRI #9(99)j78, TSG-RAN Working Group 1 meeting #9, Dresden, Germany, November 30-December 3, 1999

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DATE CONSIDERED

11/2/05

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